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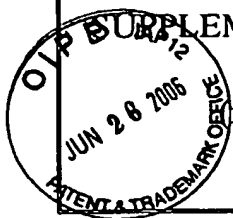
Rothman et al.

Filing Date

9/30/2003

Group Art Unit

2127

SUPPLEMENTAL INFORMATION DISCLOSURE  
STATEMENTin response to request for information  
under 37 C.F.R. § 1.105)

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

*EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

*Examiner Initials	Document Number	Publication Date	Country	Class	Subclass	Translation Yes No

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

SW	C01	"Extensible Firmware Interface Specification." Version 1.10. Intel Corporation. December 1, 2002. Pages: Ch1: 6-11; Ch2: 1-5 and 17-20; Ch3: 1-8; Ch5: 26-27; Ch8: 1; Ch11: 2-6, 12, 13, and 16; Ch12: 46; Ch19: 1-3 and 76; and Glossary: 15.				
SW	C02	"Extensible Firmware Interface Specification." Version 1.10. Intel Corporation. December 1, 2002. Pages: all. [ <a href="http://www.intel.com/technology/efi">http://www.intel.com/technology/efi</a> ]. Paper copy previously submitted via information disclosure statement on January 15, 2004.				

EXAMINER /Sean Weinman/

DATE CONSIDERED 09/11/2006

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.